1.5A LED Driver with internal switch

## .General Description

TTP933 is a high efficiency, constant current, continuous mode inductive step-down converter, designed for driving constant current to high power (single or multiple) LED with only 4 external components. TTP933 operates from input supply between 5 V and 33 V and provides an externally adjustable output current of up to 1.5 A .

The TTP933 is specifically designed with PFM control to enhance the efficiency up to $97 \%$. The Output current can be modify by an external resister, and can adjusted, by applying an external control signal to the DIM pin , The DIM pin will accept a PWM waveform.

Additionally, to ensure the system reliability, TTP933 is built-in with over temperature protection, and LED open-circuit short-circuit protection to protect system from being damaged.

## .Features

$>1.5 \mathrm{~A}$ output current
$\triangleleft$ Wide input voltage range: 5 V to 33 V
$\diamond$ High efficiency (up to 97\%)
\& Internal NDMOS power switch
« Single pin on/off and brightness control using PWM
> Hysteretic PFM improves efficiency at light loads
$\diamond$ With Thermal/Soft start/LED open-short detect protection
$\diamond$ Only 4 External Components
> Up to 1 Mhz switching frequency
> Typical 3\% output current accuracy

## .Applications

- High power LED lighting
- Automotive LED lighting
- Low voltage industrial lighting
- LED back-up lighting
- Constant Current Source


## .Package type





TTP933 BMB: MSOP8 (with Ther mal Pad)
TTP933 C08: SOP8 (with Ther mal Pad)
TTP933-D08: SOP8 (with Ther mal Pad)
TTP933- EA5: SOT23-5
TTP933- FA6: SOT23-6
TTP933- GG5: SOT89-5
TTP933- HE5: TO252-5
TTP933 J08: SOP8 ( with Ther mal Pad)

## .Pin Description

| Pin Name | Pin Description |
| :--- | :--- |
| SW | Drain of NDMOS switch |
| GND | Ground pad |
| DGND | Source of NDMOS |
| DIM | Dimming control pad |
| SEN | Isense Connect resistor RS from this pin to VIN to define nominal average <br> output current |
| VIN | Power pad |
| EN | Enable control signal, H:Active, L:Power down, Built-in pull high |

.Application circuit

## Typical applicatiom circuit

VIN

.Absolute maximum ratings

| ITEM | SYMBOL | RATING | UNIT |
| :---: | :---: | :---: | :---: |
| Supply Voltage | Vin | $0-33(40 \mathrm{~V}$ for 0.5 sec$)$ | V |
| Output Current | Iout | 1.8 | A |
| Sustaining Voltage at SW pin | Vsw | $-0.5 \sim 33$ (40V for 0.5 sec ) | V |
| Power Dissipation * | PD | 1.4 | W |
|  |  | 1.45 |  |
|  |  | 2.8 |  |
|  |  | 1.2 |  |
|  |  | 1.2 |  |
|  |  | 1.45 |  |
| Thermal SOP8 | Rth(j-a) | 89.3 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Resistance MSOP8(thermal <br> Pad) |  | 86.2 |  |
| TO252 |  | 44.6 |  |
| SOT23-6 |  | 104.2 |  |
| SOT23-5 |  | 104.2 |  |
| SOT89-5 |  | 86.2 |  |
| Operating Temperature | Top | $-40^{\circ} \mathrm{C}-+85^{\circ} \mathrm{C}$ | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | Tsto | $-55^{\circ} \mathrm{C}-+150^{\circ} \mathrm{C}$ | ${ }^{\circ} \mathrm{C}$ |
| Junction Temperature | Tj-max | 150 | ${ }^{\circ} \mathrm{C}$ |

* 2 layer The PCB size is $22 \mathrm{~mm} * 20 \mathrm{~mm}$
** Power Dissipation depend on PCB layout.


## Electrical Characteristics

Test condition ：Vin $=12 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=3.6 \mathrm{~V}, \mathrm{Ll}=68 \mathrm{uH}, \mathrm{C}_{\mathrm{IN}}=\mathrm{C}$ out $=10 \mathrm{uF}, \mathrm{Ta}=25^{\circ} \mathrm{C}$ ；unless otherwise specified．

| Item | Symbol | Condition | min。 | typ。 | Max。 | unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Operating voltage | Vin |  | 5 | － | 33 | V |
| Operating current | In | VIN＝ $5 \mathrm{~V} \sim 33 \mathrm{~V}$ | － | 1 | 2 | mA |
| Power down current | $\mathrm{I}_{\text {OFF }}$ | VIn $=5 \mathrm{~V} \sim 33 \mathrm{~V}$ ，EN pin grounded |  | 50 |  | uA |
| Output current | $\mathrm{I}_{\text {OUT }}$ |  | － | － | 1.5 | A |
| Output current Accuracy | $\Delta \mathrm{I}_{\text {out } / \text { Iout }}$ | $150 \mathrm{~mA} \leqq \mathrm{IOUT} \leq 1 \mathrm{~A}$ |  | $\pm 3$ | $\pm 5$ | \％ |
| Efficiency |  | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=350 \mathrm{~mA}, \mathrm{~V}_{\text {out }}=10.8 \mathrm{~V}$ |  | 97 |  | \％ |
| SW Dropout voltage | $\Delta$ Vsw | Iout $=1 \mathrm{~A}$ |  | 0.5 |  | V |
| Internal propagation delay | TPD |  | 100 | 200 | 300 | nS |
| Input voltage | VIH |  | 3.5 |  | 5 | V |
|  | VIL |  |  |  | 0.5 | V |
| Sense threshold hysteresis | Vsensehys |  |  | $\pm 15$ | － | \％ |
| Mean current sense threshold voltage | Vsense |  | 95 | 100 | 105 | mV |
| Switch on resistance | $\mathrm{Rds}(\mathrm{ON})$ | Vın＝12V，I ar＝350mA，Vout $=10.8 \mathrm{~V}$ |  | 0.33 |  | $\Omega$ |
| Minimum sw itch＇ON＇ time | ToNmin |  | 100 | 350 | 450 | nS |
| Minimum switch＇OFF＇ time | Toffmin |  | 100 | 350 | 450 | nS |
| Recommended duty cycle range oft switch | Dsw |  | 0.2 |  | 0.8 |  |
| maximum operating frequency | Frequax |  | 40 |  | 1000 | KHz |
| Thermal Shutdown Threshold | Tsd |  | 145 | 160 | 175 | C |
| Thermal Shutdow $n$ Hystersis | Tsd－hys |  |  | 20 |  | C |
| Duty cycle range of PWM signal applied to DIM pin | Duty ${ }_{\text {dim }}$ | PWM frequency $=1 \mathrm{KHz}$ | 0.01 |  | 1 |  |
| Rise Time of Output current | Tr | Vout $=3.6 \mathrm{~V}$ ，Iout $=350 \mathrm{~mA}$ ， <br> fdim $=1 \mathrm{kHz}$ ，Duty уim $=50 \%$ |  | 20 |  | ns |
| Fall Time of Output current | Tf | Vout $=3.6 \mathrm{~V}$ ，Iout $=350 \mathrm{~mA}$ ， <br> fdim $=1 \mathrm{kHz}$ ，Duty ${ }_{\text {dim }}=50 \%$ |  | 20 |  | ns |

.BLOCK diagram


## Device description

The device, in conjunction with the coil (L1) and current sense resistor (RS), forms a selfoscillating continuous-mode buck converter.
Device operation (Refer to block diagram and Figure 1 - Operating waveforms)
Operation can be best understood by assuming that the DIM pin of the device is unconnected and the voltage on this pin (VDIM) appears directly at the $(+)$ input of the comparator.
When input voltage VIN is first applied, the initial current in L1 and RS is zero and there is no output from the current sense circuit. Under this condition, the ( - ) input to the comparator is at ground and its output is high. This turns MN on and switches the SW pin low, causing current to flow from VIN to ground, via RS, L1 and the LED(s). The current rises at a rate determined by VIN and L1 to produce a voltage ramp (VSENSE) across RS. The supply referred voltage VSENSE is forced across internal resistor R1 by the current sense circuit and produces a proportional current in internal resistors R2 and R3. This produces a ground referred rising voltage at the (-) input of the comparator. When this reaches the threshold voltage (Vref), the comparator output switches low and MN turns off. The comparator output also drives another NMOS switch, which bypasses internal resistor R3 to provide a controlled amount of hysteresis. The hysteresis is set by R3 to be nominally $15 \%$ of VADJ.
When MN is off, the current in L1 continues to flow via D1 and the LED(s) back to VIN. The current decays at a rate determined by the LED(s) and diode forward voltages to produce a falling voltage at the input of the comparator. When this voltage returns to VADJ, the comparator output switches high again. This cycle of events repeats, with the comparator input ramping between limits of Vref $\pm 15 \%$.

## Switching thresholds

With VADJ = VREF, the ratios of R1, R2 and R3 define an average VSENSE switching threshold of 100 mV (measured on the ISENSE pin with respect to VIN). The average output current IOUTnom is then defined by this voltage and RS according to:
IOUTnom $=100 \mathrm{mV} / \mathrm{RS}$
Nominal ripple current is $\pm 15 \mathrm{mV} / \mathrm{RS}$

SW voltage

SENSE voltage

Coil current


Figure 1-Operating waveforms

## .Typical Performance Characteristics

1. Efficiency vs. Input Voltage at Various LED Cascaded Number


Efficiency vs. input voltage @L=22uH, IouT $=384 \mathrm{~mA}$


Efficiency vs. input voltage @ $\mathrm{L}=68 \mathrm{uH}$, IOUT $=384 \mathrm{~mA}$


Efficiency vs. input voltage @L=22uH, $\mathrm{I}_{\mathrm{OUT}}=769 \mathrm{~mA}$


Efficiency vs. input voltage $@ \mathrm{~L}=68 \mathrm{uH}, \mathrm{I}$ OUT $=769 \mathrm{~mA}$


Efficiency vs. input voltage @L=22uH, $\mathrm{I}_{\text {out }}=1000 \mathrm{~mA}$


Efficiency vs. input voltage @L=68uH, $\mathrm{I}_{\text {OuT }}=1000 \mathrm{~mA}$
2. Efficiency vs. LED Cascaded Number at Various Input Voltage


Efficiency vs. LED cascaded number @L=22uH, $\mathrm{I}_{\text {out }}=384 \mathrm{~mA}$


Efficiency vs. LED cascaded number @L=68uH, $\mathrm{I}_{\text {out }}=384 \mathrm{~mA}$


Efficiency vs. LED cascaded number @L=22uH, $\mathrm{I}_{\text {out }}=769 \mathrm{~mA}$


Efficiency vs. LED cascaded number @L=68uH, $\mathrm{I}_{\text {out }}=769 \mathrm{~mA}$


Efficiency vs. LED cascaded number @L=22uH, IouT $=1000 \mathrm{~mA}$


Efficiency vs. LED cascaded number @L=68uH, IouT $=1000 \mathrm{~mA}$

## 3. Output Current vs. Input Voltage at Various LED Cascaded Number



Output current vs. input voltage @ $\mathrm{L}=22 \mathrm{uH}, \mathrm{I}_{\text {out }}=384 \mathrm{~mA}$


Output current vs. input voltage @ $\mathrm{L}=68 \mathrm{uH}$, Iour $=384 \mathrm{~mA}$


Output current vs. input voltage @ $\mathrm{L}=22 \mathrm{uH}, \mathrm{I}_{\text {OUT }}=769 \mathrm{~mA}$


Output current vs. input voltage @ $\mathrm{L}=68 \mathrm{uH}$, $\mathrm{I}_{\text {out }}=769 \mathrm{~mA}$


Output current vs. input voltage @L=22uH, IouT $=1000 \mathrm{~mA}$


Output current vs. input voltage @L=68uH, IouT $=1000 \mathrm{~mA}$
4. Output Currnet vs. Input Voltage at Various Inductor


Output current vs. input voltage @1-LED in cascaded, $\mathrm{I}_{\text {out }}=384 \mathrm{~mA}$


Output current vs. input voltage @2-LED in cascaded, $\mathrm{I}_{\text {out }}=384 \mathrm{~mA}$


Output current vs. input voltage @3-LED in cascaded, Iout $=384 \mathrm{~mA}$
5. Output Current vs. LED Cascaded Number at Various Input Voltage


Output current vs. LED cascaded number @ $\mathrm{L}=22 \mathrm{uH}, \mathrm{I}_{\mathrm{OUT}}=384 \mathrm{~mA}$


Output current vs. LED cascaded number @L=68uH, IOUT $=384 \mathrm{~mA}$


Output current vs. LED cascaded number @L=22uH, $\mathrm{I}_{\text {Out }}=769 \mathrm{~mA}$


Output current vs. LED cascaded number @L=68uH, IOUT $=769 \mathrm{~mA}$
6. Output Current vs. LED Cascaded number at Various Inductor


Output Current vs. LED Cascaded number @VIN=12V, IouT $=384 \mathrm{~mA}$


Output Current vs. LED Cascaded number @VIN=24V, I IouT $=384 \mathrm{~mA}$
7. Switching Frequency vs. LED Cascaded Number at Various Inductor


Switching Frequency vs. LED Cascaded Number @VIN=12V, IouT=384mA


Switching Frequency vs. LED Cascaded Number @VIN=24V, $\mathrm{I}_{\text {OUT }}=384 \mathrm{~mA}$


Switching Frequency vs. LED Cascaded Number @VIN=30V, $\mathrm{I}_{\text {OUT }}=384 \mathrm{~mA}$
8. Dimming and switching waveforms


13 Aug 2010 13:48:28
Dimming waveform $\left(\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}, \mathrm{R}_{\mathrm{SEN}}=0.27 \Omega, 3\right.$-LED $)$


Switching waveform $\left(\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}, \mathrm{R}_{\mathrm{SEN}}=0.27 \Omega, 3\right.$-LED)

## . Application notes

## .Setting nominal average output current with external resistor RS

The nominal average output current in the $\operatorname{LED}(\mathrm{s})$ is determined by the value of the external current sense resistor (RS) connected between VIN and Iout and is given by:

IOUTnom $=0.1 /$ Rs [for $\mathrm{Rs} \geqq 0.1 \Omega$ ]
Nominal ripple current is $\pm 15 \mathrm{mV} / \mathrm{Rs}$

The table below gives values of nominal average output current for several preferred values of current setting resistor (RS) in the typical application circuit shown on page 1:

| $\operatorname{RS}(\boldsymbol{\Omega})$ | Nominal average output current (mA) |
| :--- | :--- |
| 0.1 | 1000 |
| 0.13 | 760 |
| 0.15 | 667 |

## . Dimming

A Pulse Width Modulated (PWM) signal with duty cycle DPWM can be applied to the DIM pin ., A logic low (below 0.5 V ) at DIM will disable the internal MOSFET and turn off the current Flow to the LED array. An internal pull-high circuit ensures that The TTP933 is ON when DIM pin is unconnected.

## . Open / short circuit LED protection

When any LED is open - circuit , the output current will be turned off .
When any LED is short - circuit , the output current will be limited to its preset value .

## . Over Temperature protection

When the junction temperature over range. TTP933 will turn off output Current .

## .Minimum Input Voltage

The Minimum Input Voltage is the sum of the voltage drops on Rsen, DCR of L1, Rds(on) of Internal MOS switch and the total forward voltage of LEDS VLEd Vin $=V_{R S}+V_{L E D}+V_{L 1}+V_{s w}$.

## .Design Consideration :

## .Switching Frequency

For better output current accuracy, the switching frequency should be determined by Minimum on/off time SW waveform .

Fsw $=(1-\mathrm{D}) /$ Toff,Min , when the duty cycle is large than $0.5 \quad(\mathrm{D}=$ Vout $/$ Vin $)$
or
Fsw $=\mathrm{D} / \mathrm{Ton}$, ,min , when the duty cycle is smaller than 0.5

The switching frequency is related to efficiency (better at low frequency), the size/cost Of components, and the amplitude of output ripple voltage and current ( smaller at high frequency) . The slower switching frequency comes from the large value of inductor . In many applications, the Sensitivity of EMI limits the switching frequency. The switching frequency can be ranged from 40 Khz To 1.0 Mhz .

## . LED Ripple Current

A LED constant current driver, is designed to control the current through the cascaded LED, instead of the voltage across it. Higher LED ripple current allows the use of smaller inductance, smaller output capacitance and even without an output capacitor. The advantages of higher LED ripple current are to minimize PCB size and reduce cost because of no output capacitor . Lower LED ripple current requires large induce and output capacitor. The advantages of lower LED ripple Current are to extend LED life time and to reduce heating of LED . The recommended ripple current is From $5 \%$ to $20 \%$ of normal LED output current

## Capacitor selection

A low ESR capacitor should be used for input decoupling, as the ESR of this capacitor appears in series with the supply source impedance and lowers overall efficiency. This capacitor has to supply the relatively high peak current to the coil and smooth the current ripple on the input supply.
A minimum value of 4.7 uF is acceptable if the input source is close to the device, but higher values will improve performance at lower input voltages, especially when the source impedance is high. The input capacitor should be placed as close as possible to the IC. For maximum stability over temperature and voltage, capacitors with X7R, X5R, or better dielectric are recommended. Capacitors with Y5V dielectric are not suitable for decoupling in this application and should NOT be used. A suitable Murata capacitor would be GRM42-2X7R475K-50.

## Inductor selection

The inductance is determined by two factors: the switching frequency and the inductor ripple Current. The calculation of the inductance, L1, can be described as

LI> (Vin-Vout-Vsen-(Rds(ON)x Iout))x D/( fswx $\Delta \mathrm{IL})$

Higher values of inductance are recommended at higher supply voltages in order to minimize errors due to switching delays, which result in increased ripple and lower efficiency. Higher values of inductance also result in a smaller change in output current over the supply voltage range. (See graphs). The inductor should be mounted as close to the device as possible with low resistance connections to the SW and Vin pins.

The chosen coil should have a saturation current higher than the peak output current and a continuous current rating above the required mean output current.

The inductor value should be chosen to maintain operating duty cycle and switch 'on'/'off' times within the specified limits over the supply voltage and load current range.

```
Switch on time
    Ton \(=\mathrm{L} \Delta \mathrm{I} /\left(\mathrm{V}_{\text {IN }}-\mathrm{V}_{\text {LED }}-\mathrm{I}_{\text {avg }}(\mathrm{Rs}+\mathrm{rL}+\mathrm{Rsw})\right)\)
    Toff \(=\mathrm{L} \Delta \mathrm{I} /\left(\mathrm{V}_{\mathrm{LED}}+\mathrm{V}_{\mathrm{D}}+\operatorname{Iavg}(\mathrm{Rs}+\mathrm{rL})\right)\)
```

Where
L is the coil inductance
rL is the coil resistance
Rs is the current sense resistance
Iavg is the required LED current
$\Delta \mathrm{I}$ is the coil peak-peak ripple current \{Internally set to $0.3 \times \mathrm{Iavg}\}$
Vin is the supply voltage
VLED is the total LED forward voltage
Rsw is the switch resistance
$V_{D}$ is the diode forward voltage at the required load current

## Diode selection :

For maximum efficiency and performance, the rectifier (D1) should be a fast low capacitance Schottky diode with low reverse leakage at the maximum operating voltage and temperature. They also provide better efficiency than silicon diodes, due to a combination of lower forward voltage and reduced recovery time. It is important to select parts with a peak current rating above the peak coil current and a continuous current rating higher than the maximum output load current. It is very important to consider the reverse leakage of the diode when operating above $85^{\circ} \mathrm{C}$. Excess leakage will increase the power dissipation in the device and if close to the load may create a thermal runaway condition. The higher forward voltage and overshoot due to reverse recovery time in silicon diodes will increase the peak voltage on the SW output. If a silicon diode is used, care should be taken to ensure that the total voltage appearing on the SW pin including supply ripple, does not exceed the specified maximum value.

## Package Information

SOT23-6 (TTP933-FA6)



SECTION "B"-"B"

NDTES:
1.DIMENSION D1 \& E1 DOES NOT INCLUDE MOLD PROTRUSION.
2.COPLANARITY DF ALL LEADS SHALL BE (BEFORE TEST) 0.1 MAX. FROM THE SEATING PLANE. UNLESS GTHERWVISE SPECIFED.
3.GENERAL PHYSIGAL DUTLINE SPEC IS REFER TO TMC'S FINAL WSUAL INSPECTION SPEC UNLESS OTHERWISE SPECIFIED.


DETAIL "A" $\{\mathbf{S}=32: 1\}$

TO252-5(TTP933-HE5)



### 40.8.8

MSOP8(TTP933-BM8)


| S'KMESL | CIMIENSION IN MMA |  |  | DINENSION $1 \mathrm{~N} \mid \mathbb{N C H}$ |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | M\|l|ly. | HOM, | Mbs | $\underline{W} \mid \mathbb{N}$. | HOM. | MAX. |
| A | 0.81 | 1.02 | 1.10 | 0.032 | 0.040 | 0.043 |
| A1 | 0.05 |  | 0.15 | 0.002 |  | 0.006 |
| A2 | 0.76 | 0.85 | D. 35 | 0.030 | 0.034 | 0.057 |
| b | 0.28 | 0.30 | 0. 38 | 0.011 | 0.012 | 0.015 |
| C | 0.13 | 0.15 | 0.25 | 0.005 | 0.006 | 0.009 |
| D | 2.90 | 3.00 | 3.10 | 0.114 | 0.118 | 0.122 |
| E | 4.75 | 4.90 | 5.05 | 0.187 | 0.193 | 0.199 |
| E1 | 2.90 | 3.00 | 3.10 | 0.114 | 0.118 | 0.122 |
| ${ }^{\text {e }}$ | 0.65 PASC |  |  | 0.02 BASLC |  |  |
| L | 0.40 | 0.55 | 0.70 | 0.015 | 0.022 | [.02品 |
| $\theta$ | (f) | 3 | 6 | $0{ }^{\text {a }}$ | 3 | 6 |
| JWEL |  |  |  |  |  |  |


 OR GHIE EARRS.






SOP8 (TTP933-CO8, TTP933-D08, TTP933-JO8)


| SYMBOLS | MIN. | MAX. |
| :---: | :---: | :---: |
| $A$ | 0.053 | 0.069 |
| $A 1$ | 0.004 | 0.010 |
| $A 2$ | - | 0.059 |
| $D$ | 0.189 | 0.196 |
| $E$ | 0.150 | 0.157 |
| $H$ | 0.228 | 0.244 |
| L | 0.016 | 0.050 |
| $\theta^{\circ}$ | 0 | 8 |


E.P. VERSION ONLY


NOTES:
1.JEDEC OUTINE: MS-012 M / E.P. VERSION : N/h 2.DINENSIONS "D" DOES NOT INCLUDE MOLD FLSH, PROTRUSONS OR GATE BURRS MOLD FLASH, PROTRUSIONS AND GATE BURRS SHALL NOT EXCEED .15 mm (.006in) PER SIDE.
3.DINENSIONS "E" DOES NOT INCLUDE INIER-LEAD FLASH, OR PROTRUSIONS. INTER-LEAD FLASH AND PROTRUSIONS SHALL NOT EXCEED 25 mm (.010in) PER SIDE.

SOT89-5(TTP933-GG5)


| Symbol | Dimensions In Millimeters |  | Dimensions In Inches |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min | Max | Min | Max |  |
| A | 1.400 | 1.600 | 0.055 | 0.063 |  |
| b | 0.320 | 0.520 | 0.013 | 0.020 |  |
| b1 | 0.360 | 0.560 | 0.014 | 0.022 |  |
| c | 0.350 | 0.440 | 0.014 | 0.017 |  |
| D | 4.400 | 4.600 | 0.173 | 0.181 |  |
| D1 | 1.400 | 1.800 | 0.055 | 0.071 |  |
| E | 2.300 | 2.600 | 0.091 | 0.102 |  |
| E1 | 3.940 | 4.250 | 0.155 | 0.167 |  |
| e | 1.500 TYP |  |  | 0.060 TYP |  |
| e1 | 2.900 | 3.100 | 0.114 | 0.122 |  |
| L | 0.900 | 1.100 | 0.035 | 0.043 |  |

SOT23-5 (TTP933-EA5)


| DIM | Millimeters |  | Inches |  |
| :---: | :---: | :---: | :---: | :---: |
|  | Min. | Max. | Min. | Max. |
| A | - | 1.00 | - | 0.0393 |
| A1 | 0.01 | 0.10 | 0.0003 | 0.0039 |
| A2 | 0.84 | 0.90 | 0.0330 | 0.0354 |
| b | 0.30 | 0.45 | 0.0118 | 0.0177 |
| c | 0.12 | 0.20 | 0.0047 | 0.0078 |
| D | 2.90 BSC |  | 0.114 BSC |  |
| E | 2.80 BSC |  | 0.110 BSC |  |
| E1 | 1.60 BSC |  | 0.062 BSC |  |
| e | 0.95 BSC |  | 0.0374 BSC |  |
| e1 | 1.90 BSC |  | 0.0748 BSC |  |
| L | 0.30 | 0.50 | 0.0118 | 0.0196 |
| L2 | 0.25 BSC |  | 0.010 BSC |  |
| $\mathrm{a}^{\circ}$ | $4^{\circ}$ | $12^{\circ}$ | $4^{\circ}$ | $12^{\circ}$ |

## .ORDER INFORMATION

# 1. Package form: TTP933-BM8, TTP933-CO8, TTP933-DO8, TTP1933-EA5, TTP933-FA6, TTP933-GG5, TTP933-HE5, TTP933-JO8 <br> 2. Chip form: TCP933 

## Revise History

2013/03/05: Original version V1.0.
2013/05/02: Modify Package Information V1.1.
2013/08/23: Modify Package Information V1.2.

