



8 KEYS TOUCH PAD DETECTOR IC

GENERAL DESCRIPTION

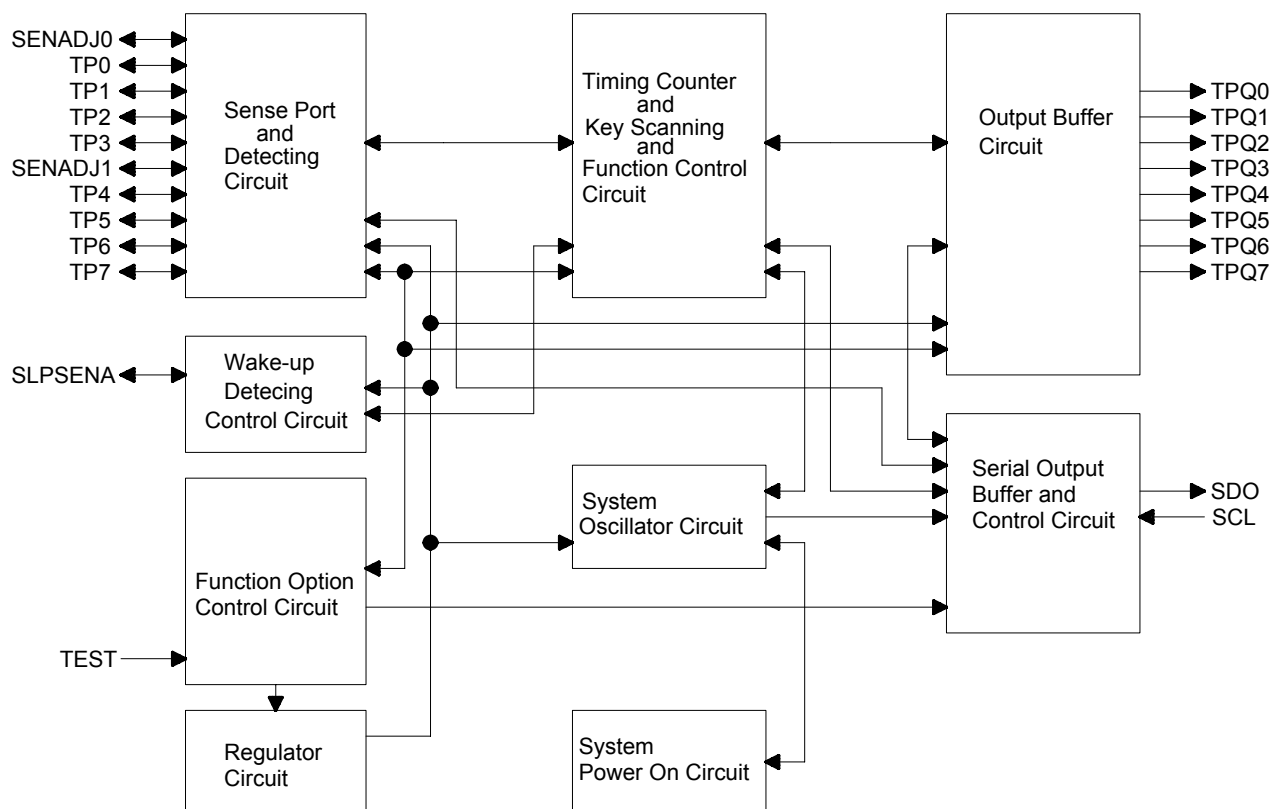
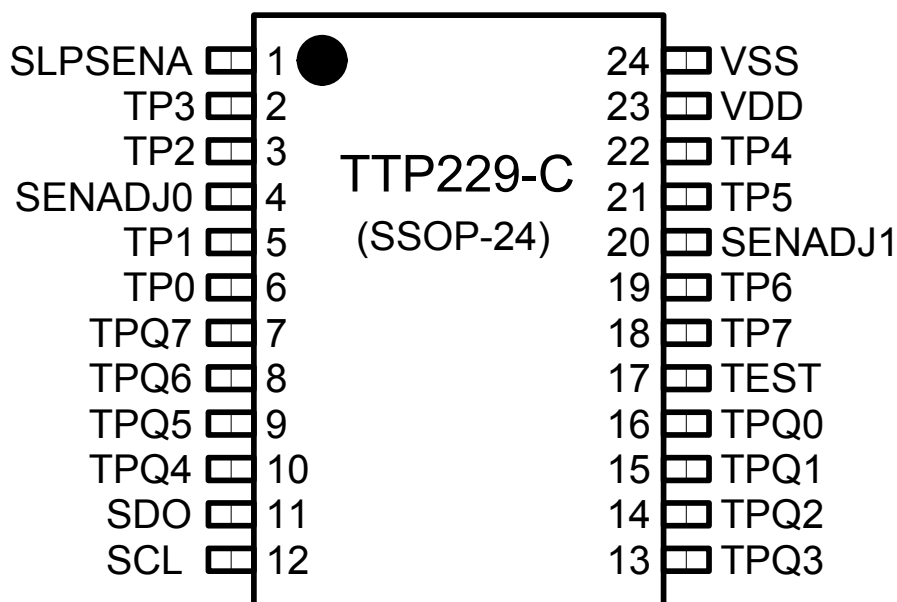
The TTP229-CSE TonTouch™ IC is capacitive sensing design specifically for touch pad controls. The device built in regulator for touch sensor. Stable sensing method can cover diversity conditions. Human interfaces control panel links through non-conductive dielectric material. The main application is focused at replacing of the mechanical switch or button. The ASSP can independently handle the 8 touch pads.

FEATURES

- Operating voltage : 2.4V~5.5V
- Built-in regulator
- Stand-by current
At 3V, and sleep mode slow sampling rate 8Hz : => Typical 2.0uA
- Provides to set 8 separate outputs for 8 direct input keys
- Has a 2-wires serial output interface
- 8 separate outputs can select output driving types by option (CMOS/OD/OC with active high/low)
- 2-wires serial interface can select active high or low by option
- Offer multi-key or single-key feature by option
- Provides two kinds of sampling rate that slow sampling rate 8Hz and fast sampling rate 64Hz at sleep mode
- Have the maximum key-on time about 80sec by pin option
- Sensitivity can adjust by the capacitance(1~50pF) outside
- After power-on have about 0.5sec stable-time,
During the time do not touch the key pad, and all functions are disabled
- Auto calibration for environment changing
And the re-calibration period is about 4.0sec, when all keys are not activated for fixed time

APPLICATION

- Wide consumer products
- Button key replacement

**BLOCK DIAGRAM****PACKAGE CONFIGURATION**

**PIN DESCRIPTION**

Pin No.	Pin Name	Share Pin	I/O Type	Pin Description
1	SLPSENA		I/O	Sleep mode sensitivity adjustment pin for group-A(TP0~7)
2	TP3	SKMS1	I/O	Touch pad input pin(KEY-3) Key action function option-1(Single-key/Multi-key) Default is all single-key
3	TP2		I/O	Touch pad input pin(KEY-2)
4	SENADJ0		I/O	Touch pad TP0~3 sensitivity adjust common pin
5	TP1	SAHL	I/O	Touch pad input pin(KEY-1) Output type function option(Active High/Low) Default is active-high for TPQ0~7, active-low for 2-wires serial type(SCL and SDO)
6	TP0	OPDEN	I/O	Touch pad input pin(KEY-0) Output type function option(CMOS/OD/OC for 8-keys) Default is CMOS
7	TPQ7		O/OD	8-keys direct output pin
8	TPQ6		O/OD	8-keys direct output pin
9	TPQ5		O/OD	8-keys direct output pin
10	TPQ4		O/OD	8-keys direct output pin
11	SDO		O	Data pin for the 2-wires serial output, option active Low/High by TP1
12	SCL		I	Serial clock input pin for serial type It can be set active Low/High by TP1
13	TPQ3		O/OD	8-keys direct output pin
14	TPQ2		O/OD	8-keys direct output pin
15	TPQ1		O/OD	8-keys direct output pin
16	TPQ0		O/OD	8-keys direct output pin
17	TEST		I-PL	Only for test
18	TP7	SKSRT	I/O	Touch pad input pin(KEY-7) Maximum key-on time function option(Infinite/80sec) Default is infinite
19	TP6	SLWPTM	I/O	Touch pad input pin(KEY-6) Sleep mode sampling length function option(4.0/2.0mS) Default is 4.0ms
20	SENADJ1		I/O	Touch pad TP4~7 sensitivity adjust common pin



Preliminary

TTP229-CSE
TonTouch™

Pin No.	Pin Name	Share Pin	I/O Type	Pin Description
21	TP5	WPST	I/O	Touch pad input pin(KEY-5) Sampling rate at sleep mode function option(8Hz/64Hz) Default is 8Hz
22	TP4	SKMS0	I/O	Touch pad input pin(KEY-4) Key action function option-0(Single-key/Multi-key) Default is all single-key
23	VDD		P	Positive power supply
24	VSS		P	Negative power supply, ground

Note : Pin Type

I => CMOS input only

I-PH => CMOS input and pull-high resister

I-PL => CMOS input and pull-low resister

O => CMOS push-pull output

I/O => CMOS I/O

P => Power / Ground

OD => CMOS open drain output

(For OD TPQ0~TPQ7 pins have Diode protective circuit, SDA pin has no Diode protective circuit)

**ELECTRICAL CHARACTERISTICS****• Absolute Maximum Ratings**

Parameter	Symbol	Conditions	Value	Unit
Operating Temperature	T _{OP}	—	-40 ~ +85	°C
Storage Temperature	T _{STG}	—	-50 ~ +125	°C
Power Supply Voltage	VDD	Ta=25°C	VSS-0.3 ~ VSS+6.0	V
Input Voltage	V _{IN}	Ta=25°C	VSS-0.3 to VDD+0.3	V
Human Body Mode	ESD	—	6	KV
Note : VSS symbolizes for system ground				

• DC/AC Characteristics : (Test condition at room temperature=25°C)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Operating Voltage	VDD		2.4	-	5.5	V
Internal Regulator Output	VREG		2.2	2.3	2.4	V
Operating Current (no load)	I _{OP}	VDD=3.0V		20		uA
Stand-by Current (VDD=3.0V) (Sampling length 4.0mS)	I _{SD}	Sampling rate 8Hz		2.0		uA
		Sampling rate 64Hz		5.5		
Input Ports	V _{IL}	Input Low Voltage	0	-	0.2	VDD
Input Ports	V _{IH}	Input High Voltage	0.8	-	1.0	VDD
Output Port Sink Current	I _{OL}	VDD=3V, V _{OL} =0.6V	-	8	-	mA
Output Port Source Current	I _{OH}	VDD=3V, V _{OH} =2.4V	-	-4	-	mA
Wake-up Response Time (at sleep mode)	T _{WU}	VDD=3V, Sampling rate 8Hz		125		mS
		VDD=3V, Sampling rate 64Hz		15.6		mS
Output Response Time (at operation)	T _R	VDD=3V		16		mS
Maximum Key-on Time	T _{MOT}		50	80	110	Sec
Input Pin Pull-low Resistor (TEST)	R _{PL}	VDD=3V		30K		ohm

FUNCTION DESCRIPTION

1. Sensitivity adjustment

The total loading of electrode size and capacitance of connecting line on PCB can affect the sensitivity. So the sensitivity adjustment must according to the practical application on PCB. The TTP229-CSE offers some methods for adjusting the sensitivity outside.

1-1 by the electrode size

Under other conditions are fixed. Using a larger electrode size can increase sensitivity. Otherwise it can decrease sensitivity. But the electrode size must use in the effective scope.

1-2 by the panel thickness

Under other conditions are fixed. Using a thinner panel can increase sensitivity. Otherwise it can decrease sensitivity. But the panel thickness must be below the maximum value.

1-3 by the value of external capacitor (please see the down Figure 1-3-1)

Under other conditions are fixed. When adding the values of CJ0~CJ1 and CJWA will reduce sensitivity in the useful range ($1\text{pF} \leq \text{CJ0} \sim \text{CJ1} \leq 50\text{pF}$, $1\text{pF} \leq \text{CJWA} \leq 50\text{pF}$). When do not use any capacitor that means open on the position of capacitor, the sensitivity is most sensitive. The capacitors CJ0~CJ1 are used to adjust the sensitivity of keys at operation mode. The capacitors CJWA is used to adjust the Wake-up sensitivity at sleep mode.

About the relation of capacitor and controlled keys please to see below table.

The capacitor	The keys-group controlled and adjusted
CJ0	K0~K3 group
CJ1	K4~K7 group
CJWA	K0~K7 group

Note : When using the value of capacitor to adjust the sensitivity, recommending to adjust the CJ0~CJ1 capacitor for K0~K7 first, then adjusting the CJWA capacitor for Wake-up sensitivity.

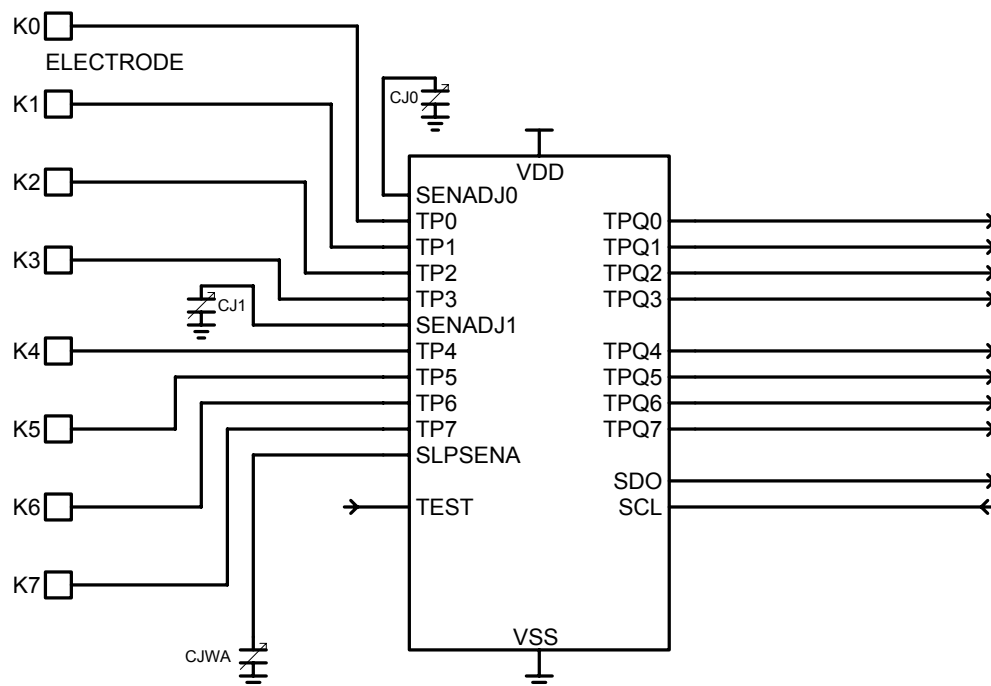


Figure 1-3-1

2. Input keys number

The TTP229-CSE has only 8 keys input mode.

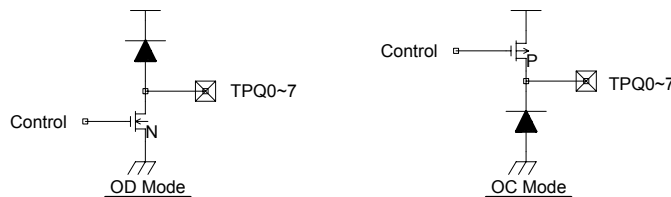
3. Output mode

The TTP229-CSE has 8 pins direct output mode and a serial output interface mode.

3-1 At the 8 pins direct output mode, the TTP229-CSE has two kinds of output type that they are CMOS type output and OD (Open Drain) type output. These are selected by the TP0(OPDEN) pin. The CMOS type output is default that the TP0(OPDEN) pin is not used any component to VSS. When the TP0(OPDEN) pin is used a high-value resistor connected to VSS, it is selected OD type output.

3-2 When selecting 8 pins direct CMOS output mode, the output channels can be set active-high or active-low by TP1(SAHL) pin. The default that the TP1(SAHL) pin is not used a high-value resistor, it is set active-high. When the TP1(SAHL) pin has a high-value resistor connected to VSS, it is set active-low.

3-3 At 8 pins direct OD output mode, it has OD (Open Drain) or OC (Open collector) output mode to be selected by the TP1(SAHL) pin. The TP1(SAHL) pin has a high-value resistor connected to VSS, it is selected OC mode. Another it is selected OD mode that does not has a resistor. The default is OD mode. The states of OD mode are floating and active-low. And the states of OC mode are floating and active-high. The structure of OD and OC output mode please to see the below figure.



Note : the output pins have Diode protective circuit in the chip. So when it selected OD or OC mode. Do not propose to connect other device that uses the different voltage. That avoids to occurring the leakage current in the system.

3-4 At the serial output interface mode the SDO pin is data output pin, the SCL is clock input pin, both can be set active-high and active-low by TP1(SAHL) pin. The default is active-low that TP1(SAHL) pin is not used resistor connected to VSS. Another it is active-high that is used a high-value resistor connected to VSS.

The 2-wires serial mode supports always polling data for other device on the system. Or other device can wait that TTP229-CSE outputs the data valid (DV) signal by the SDO pin, and it can give the clock signal to TTP229-CSE SCL pin and get the key data from SDO pin.

The TTP229-CSE 2-wires serial interface supports a timeout mechanism for SCL pin. If the SCL pin has no signal edge change over 2ms, the 2-wires serial interface will reset itself and return to stand-by state.

2-wires serial interface mode timing please see below :

The D0~D7 correspond to data of the TP0~TP7.

3-4-1. When TP1=0 : Set 8-keys active-high

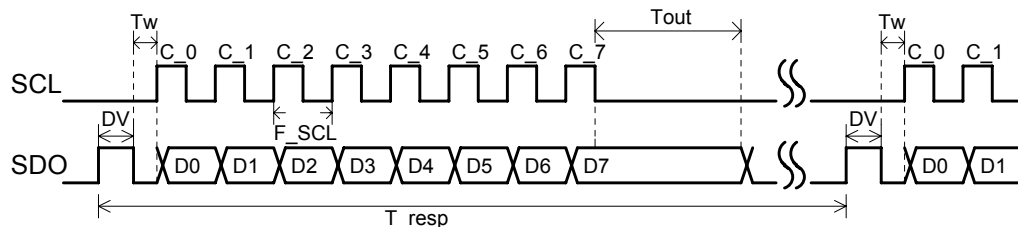


Figure 3-4-1: The timing for active-high

3-4-2. When TP1=1 : Set 8-keys active-low

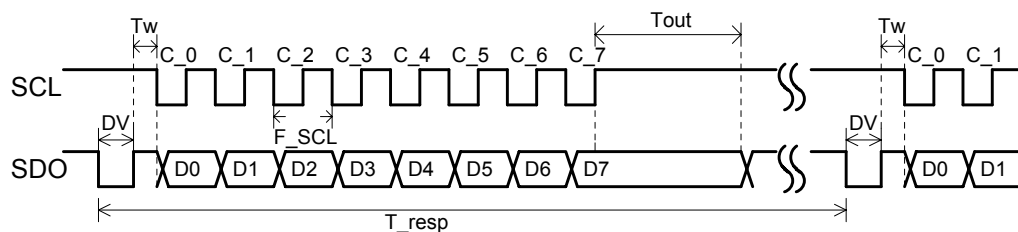


Figure 3-4-2: The timing for active-low

The parameters for figure 3-4-1~2:

Parameter	Min.	Typ.	Max.	Unit
DV	-	93	-	us
Tw	10	-	-	us
Tout	-	2	-	ms
T_resp	-	16	-	ms
F_SCL	1K	-	512K	Hz



4. Key operating mode

The TTP229-CSE has the Single-key and Multi-key functions. These functions are set by TP3(SKMS1) and TP4(SKMS0) pins. The all 8 keys can use one group, or the 8 keys can distributed into two groups. The group-1 includes TP0, TP1, TP2, TP3 keys. The group-2 includes TP4, TP5, TP6, TP7 keys. How to set the function? Please see below table 4-1 :

Table 4-1. The functions of TP3(SKMS1) and TP4(SKMS0) option

TP3 (SKMS1)	TP4 (SKMS0)	Operating function
1	1	All Single-keys : one group(8keys)
1	0	Two groups operate : group-1=>Single key ; group-2=>Single key
0	1	Two groups operate : group-1=>Single key ; group-2=>Multi key
0	0	All Multi-keys : one group(8 keys)

Note : 1. One group : TP0~TP7.

Two groups : Group-1=>TP0,TP1,TP2,TP3.

Group-2=>TP4,TP5,TP6,TP7.

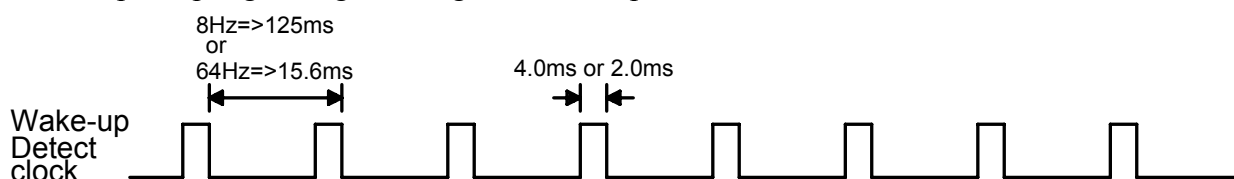
- The option states of TP3 and TP4, the "0" state is used a high-value resistor connected to VSS, the "1" state is not used resistor connected to VSS.
- The key detection acknowledges in Single-key function, the priority is by the key scanning order (from TP0 to TP7) when many keys are touched effectively. It is not by the key touching strength.

5. Wake-up sampling rate and sampling length at sleep mode

The TTP229-CSE has two kinds of sampling rate at sleep mode. These are 8Hz and 64Hz. The two functions are selected by TP5(SLWPTM) pin. The TP5(SLWPTM) pin has used a high-value resistor connected to VSS, it selected the 64Hz sampling rate. Another it is 8Hz that is not used resistor connected to VSS. The 8Hz sampling is the default.

And TTP229-CSE has two kinds of sampling length at sleep mode. They are 4ms and 2ms that are selected by TP6(WPSCT) pin. The default is 4ms that TP6(WPSCT) pin is not used resistor connected to VSS. Another it is 2ms that TP6 pin has used a high-value resistor connected to VSS.

Wake-up sampling timing and length in the sleep mode :



6. Maximum key-on time

If some objects cover in the sense pad, and causing the change quantity enough to be detected. To prevent this, the TTP229-CSE sets a timer to monitor the detection. The timer is the maximum key-on time. It is set about 80sec at 3V. When the detection is over the timer, the system will return to the power-on initial state, and the output becomes inactive until the next detection. The function is set via a high-value resistor connected to the TP7(SKSRT) pin to VSS. The TP7(SKSRT) pin does not has the resistor, it is set disable the maximum key-on time, then the key acts infinitely, this is the default. Another it is set enable the maximum key-on time that has a resistor.

If the system needs to use the maximum key-on time enable and 2-wires serial output interface function, it only uses always polling data for 2-wires serial output interface.

7. Built-in regulator

The capacitive sensing touch pad IC needs stable power. So the TTP229-CSE built in regulator in the chip. It can make the internal power to keep up steady. And the sensitivity detection is normal for chip. And the stable power can avoid sensitivity anomalies and false detections.

8. Auto calibration function

The TTP229-CSE includes a full auto-calibration function. After the device is powered-on, it will calibrate the initial condition of environment first. On the duration time all the functions are disabled, so do not operate. Then the system is into stand-by mode. And all keys are not detected touch more than about 4 seconds, then the system do re-calibration automatically. The procedure is fixed and repeated. By implementing this feature the system can catch the conditions of environment changing. And let operation of the system is normal.

9. The timing from sleep mode to operation mode

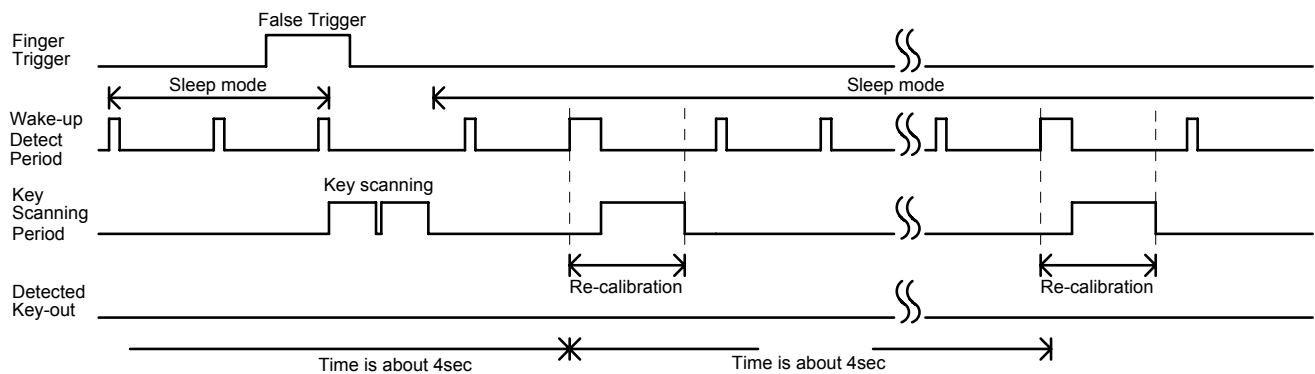


Figure 9-1. The timing for false trigger

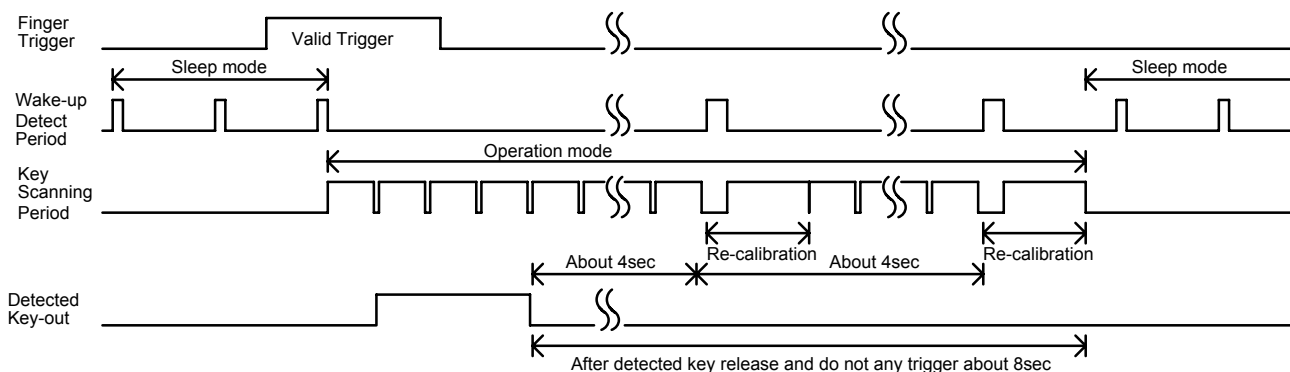


Figure 9-2. The timing for valid trigger

**10. Option table****Option table :**

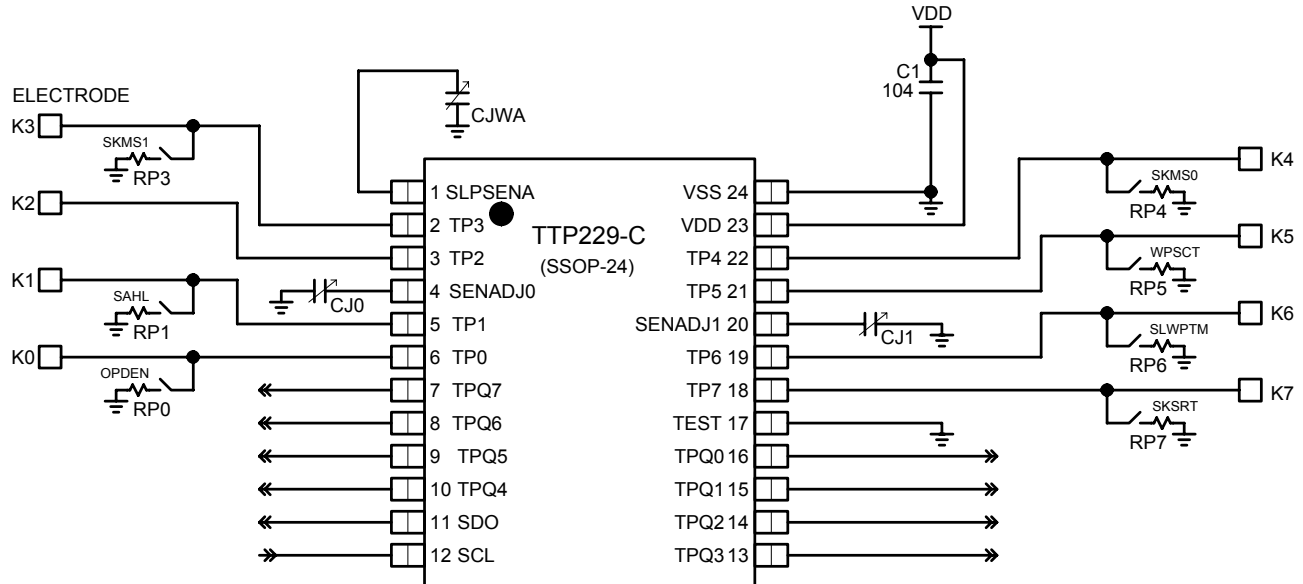
Option pin	Option states		Feature	Remark
TP0 (OPDEN)	TP0 (OPDEN)	TP1 (SAHL)	8 output pins => CMOS output active-high 2-wires serial interface => CMOS output active-low	Default
	1	1		
TP1 (SAHL)	1	0	8 output pins => CMOS output active-low 2-wires serial interface => CMOS output active-high	
	0	1	8 output pins => OD output active-low 2-wires serial interface => CMOS output active-low	
	0	0	8 output pins => OC output active-high 2-wires serial interface => CMOS output active-high	
TP3 (SKMS1)	TP3 (SKMS1)	TP4 (SKMS0)	All Single-keys : one group(8keys)	Default
	1	1		
TP4 (SKMS0)	1	0	Two groups operate : group-1=>Single key ; group-2=>Single key	
	0	1	Two groups operate : group-1=>Single key ; group-2=>Multi key	
	0	0	All Multi-keys : one group(8 keys)	
TP5 (WPSCT)	1		8Hz sampling rate for wake-up in sleep mode	Default
	0		64Hz sampling rate for wake-up in sleep mode	
TP6 (SLWPTM)	1		Wake-up sampling length=>about 4.0ms	Default
	0		Wake-up sampling length=>about 2.0ms	
TP7 (SKSRT)	1		Maximum key-on time disable=>infinite	Default
	0		Maximum key-on time enable=>about 80sec	

Note : 1. About the combinations of group-1 and group-2, please see above point-4.

2. Option states “1” mean internal pull-up (default).

3. Option states “0” mean that option pins are via high-value resistors connected to VSS.

APPLICATION CIRCUIT



- PS :
1. On PCB, the length of lines from touch pad to IC pin shorter is better.
And the lines do not parallel and cross with other lines.
 2. The power supply must be stable. If the supply voltage drift or shift quickly, maybe causing sensitivity anomalies or false detections.
 3. The material of panel covering on the PCB can not include the metal or the electric element.
The paints on the surfaces are the same.
 4. The C1 capacitor must be used between VDD and VSS; and should be routed with very short tracks to the device's VDD and VSS pins (TTP229-CSE).
 5. The capacitance CJ0~CJ1 and CJWA can be used to adjust the sensitivity. The value of capacitors use smaller, then the sensitivity will be better. The sensitivity adjustment must according to the practical application on PCB. The range value of capacitors are $1\text{pF} \leq \text{CJ0} \sim \text{CJ1} \leq 50\text{pF}$, $1\text{pF} \leq \text{CJWA} \leq 50\text{pF}$. Recommend to adjust the CJ0~CJ1 capacitor for K0~K7 first, then adjusting the CJWA capacitor for Wake-up sensitivity.
 6. The sensitivity adjustment capacitors (CJ0~CJ1, CJWA) must use smaller temperature coefficient and more stable capacitors. Such as X7R, NPO for example. So for touch application, recommend to use NPO capacitor, for reducing that the temperature varies to affect sensitivity.
 7. Recommend to use 820K ohm resistor for RP0, RP1, RP3~RP7 resistors.
 8. When the system does not use the serial output interface, the SCL pin must be connected to VSS or VDD, it is not allowed floating.



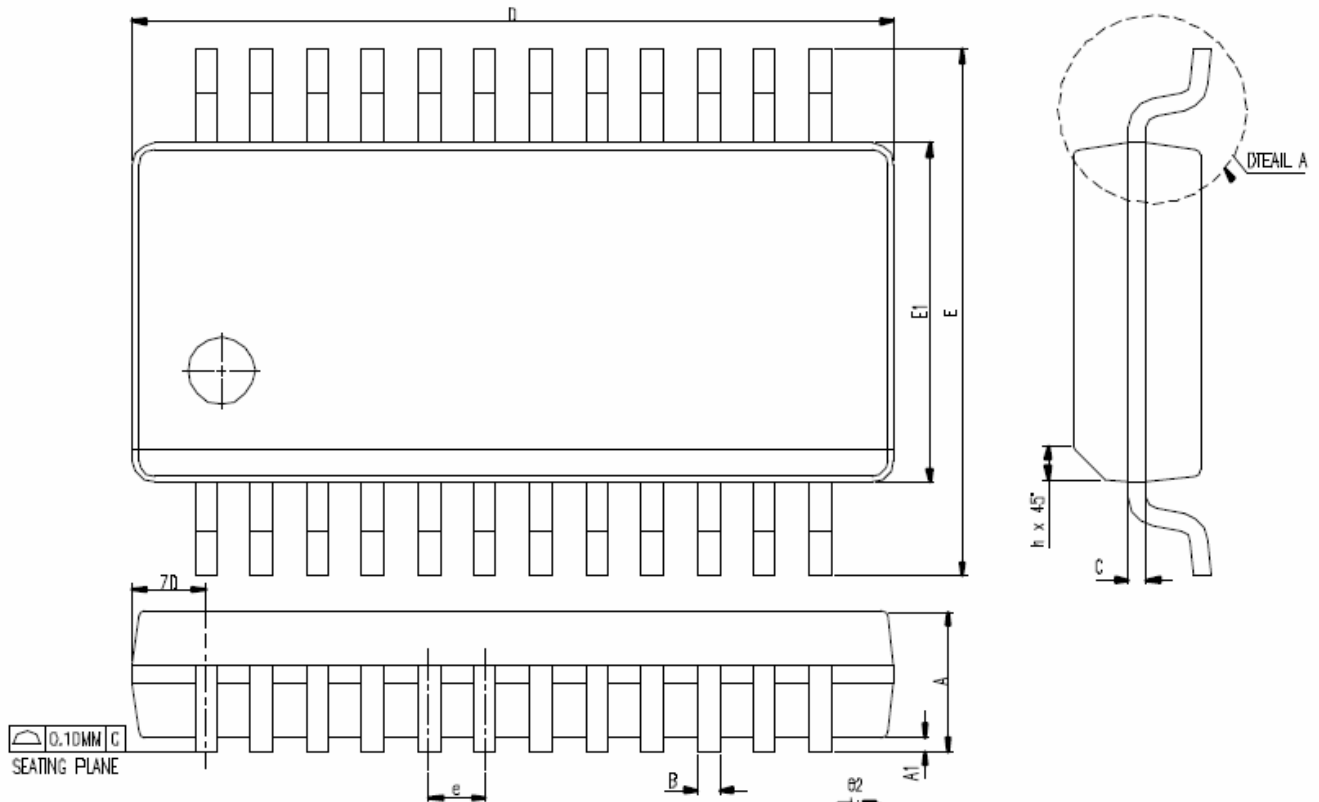
Preliminary

TTP229-CSE
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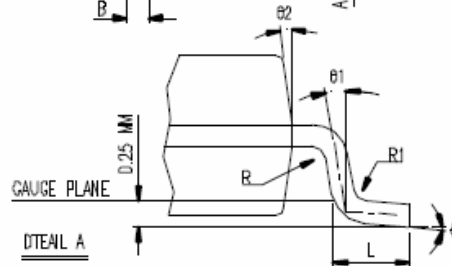
PACKAGE OUTLINE (24 PIN SSOP)

Package Type: SSOP-24

Package Outline Dimension



SYMBOL	DIMENSION IN MM			DIMENSION IN INCH		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	1.35	1.63	1.75	0.053	0.064	0.069
A1	0.10	0.15	0.25	0.004	0.006	0.010
A2			1.50			0.059
B	0.20		0.30	0.008		0.012
c	0.18		0.25	0.007		0.010
e	0.635 BASIC			0.025 BASIC		
D	8.56	8.66	8.74	0.337	0.341	0.344
E	5.79	5.99	6.20	0.228	0.236	0.244
E1	3.81	3.91	3.99	0.150	0.154	0.157
L	0.41	0.635	1.27	0.016	0.025	0.050
h	0.25		0.50	0.010		0.020
ZD	0.838 REF.			0.033 REF.		
R1	0.20		0.33	0.008		0.013
R	0.20			0.008		
θ	0°		8°	0°		8°
θ1	0°			0°		
θ2	5°	10°	15°	5°	10°	15°
JEDEC	MO-137 (AE)					



△*NOTES : DIMENSION D DOES NOT INCLUDE MOLD PROTRUSIONS OR GATE BURRS.
MOLD PROTRUSIONS AND GATE BURRS SHALL NOT EXCEED 0.006 INCH PER SIDE.



ORDER INFORMATION

a. Package form: TTP229-CSE

REVISE HISTORY

1. 2009/12/03

-Original version : V_1.0

2. 2009/12/22 =>V_1.1

-Adding some descriptions for function.

A. Adding the description of Stand-by current at the page-5.

B. Adding the figure and description for OD and OC output mode at the page-7 point 3-3

C. Adding the description of Single-key function at the page-9 point 4 note 3

D. Changing and adding the Maximum Key-on Time value at the page-1, 3, 5, 9, 11.

E. Changing the description of Table 4-1 at the page-9.

F. Changing the values for RP0, RP1, RP3~RP7 resistors from 1M to 820K at page-12 PS: 7.